

FIG. 2

PRIOR ACT

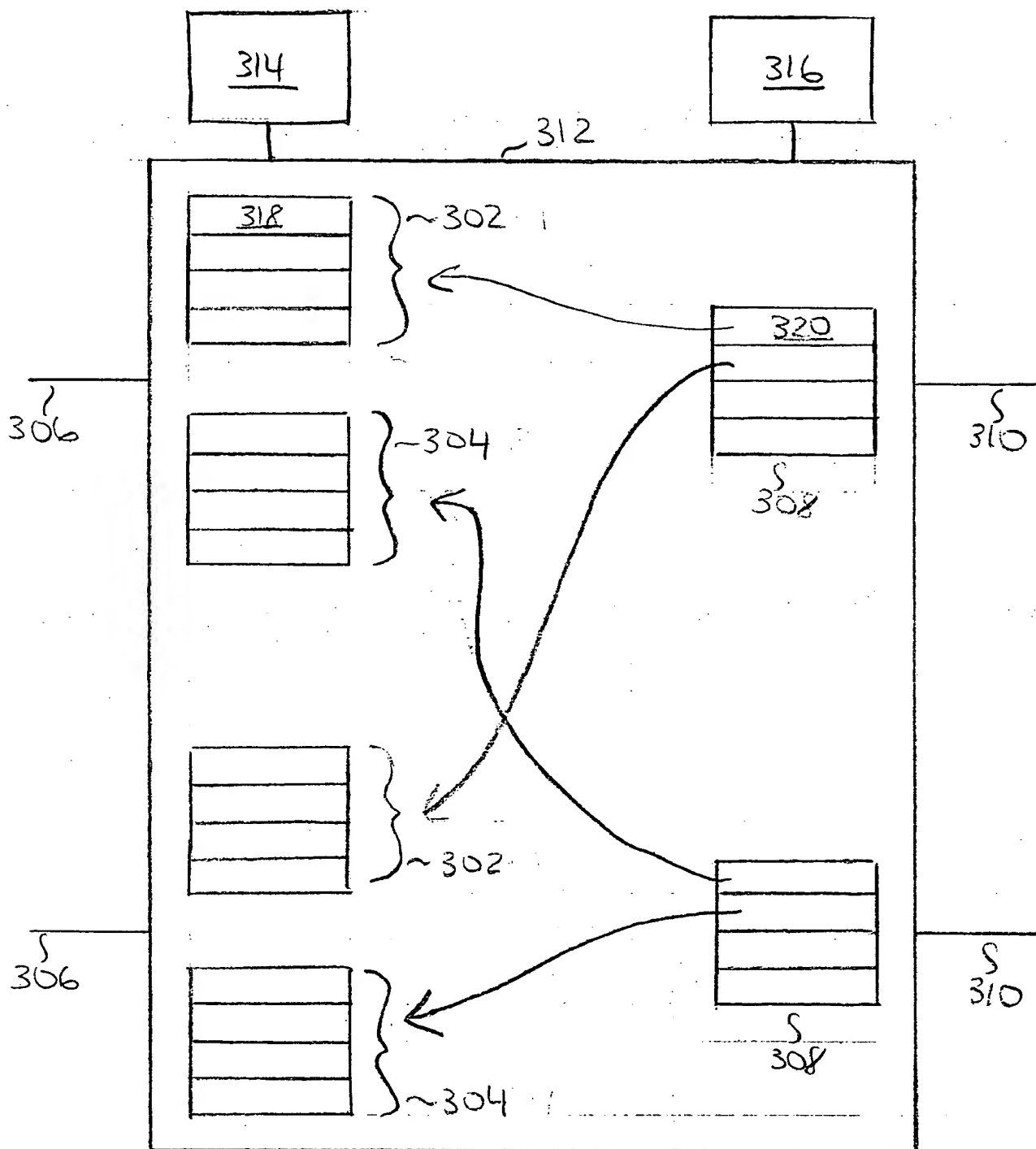
[illegible]

FIG. 3

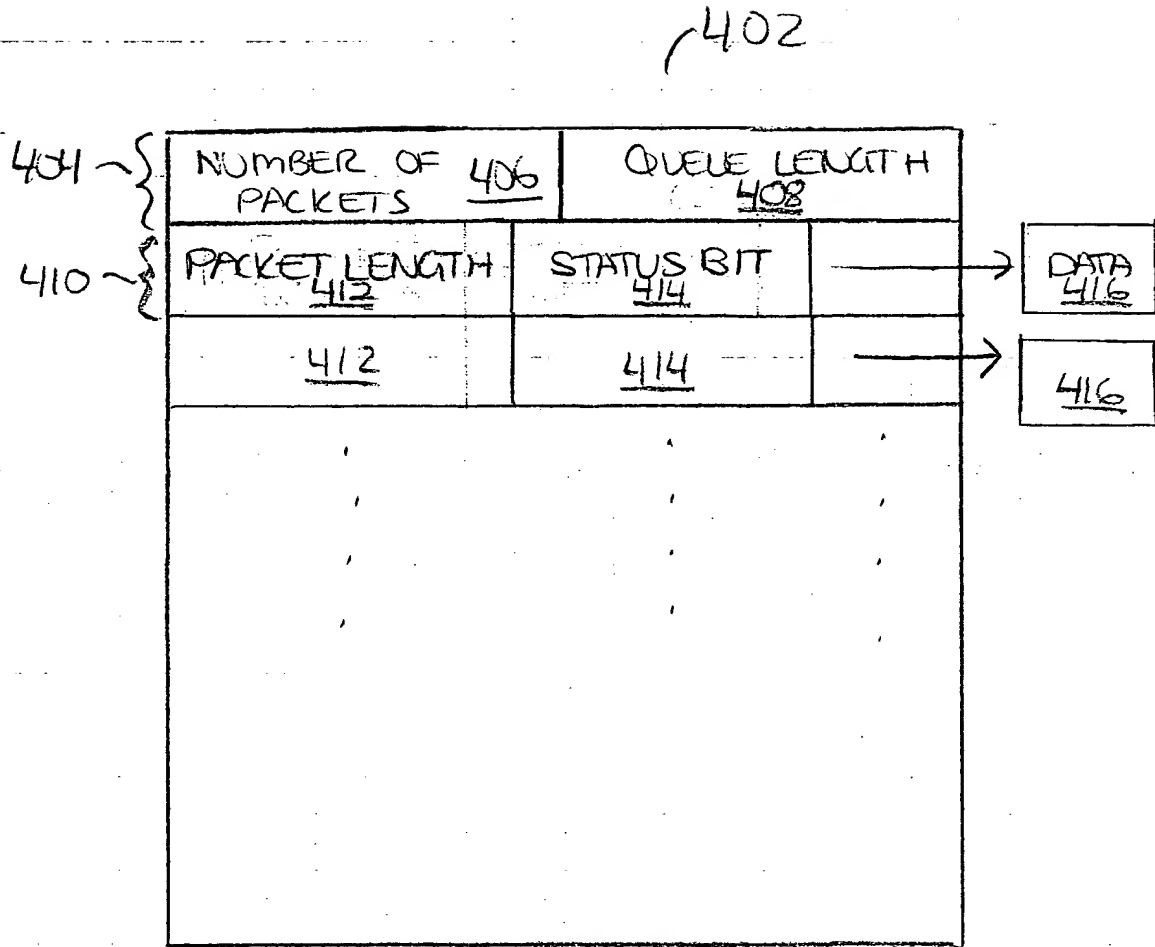


FIG. 4

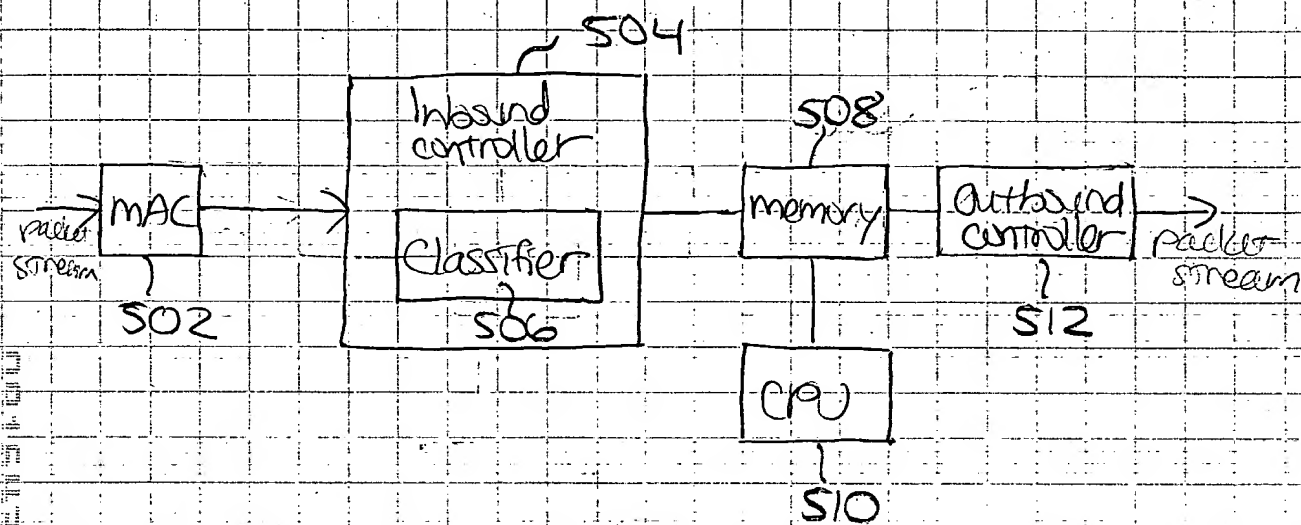


FIG. 5

BEGIN
INBOUND
CONTROLLER

Receive
inbound packet

~602

Classify the
inbound packet in
an inbound queue

~604
FIG. 7

Store the inbound
packet in the
inbound queue

~606
FIG. 9

Determine whether
to wake up CPU

~608
FIG. 10

Wake
up
CPU?

~610

Assert interrupt to
wake up CPU

~612

DONE

FIG. 6

Begin classifier

604

Select inbound packet sorting criteria (e.g., dest. add, priority)

702

Obtain packet sorting data corr. to the packet sorting criteria

704

Determine an appropriate inbound queue by locating the sorting data in an address table

706

Done

FIG. 7

Queue ID	Dest. Add	Priority
A	Port 3	2
B	Port 4	1
C	Port 3	1
D	Port 4	2

FIG. 8

606

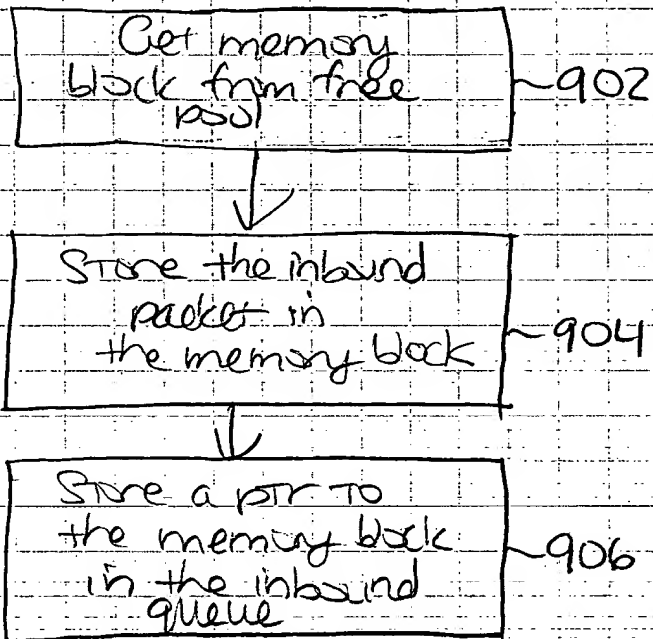


FIG. 9

608

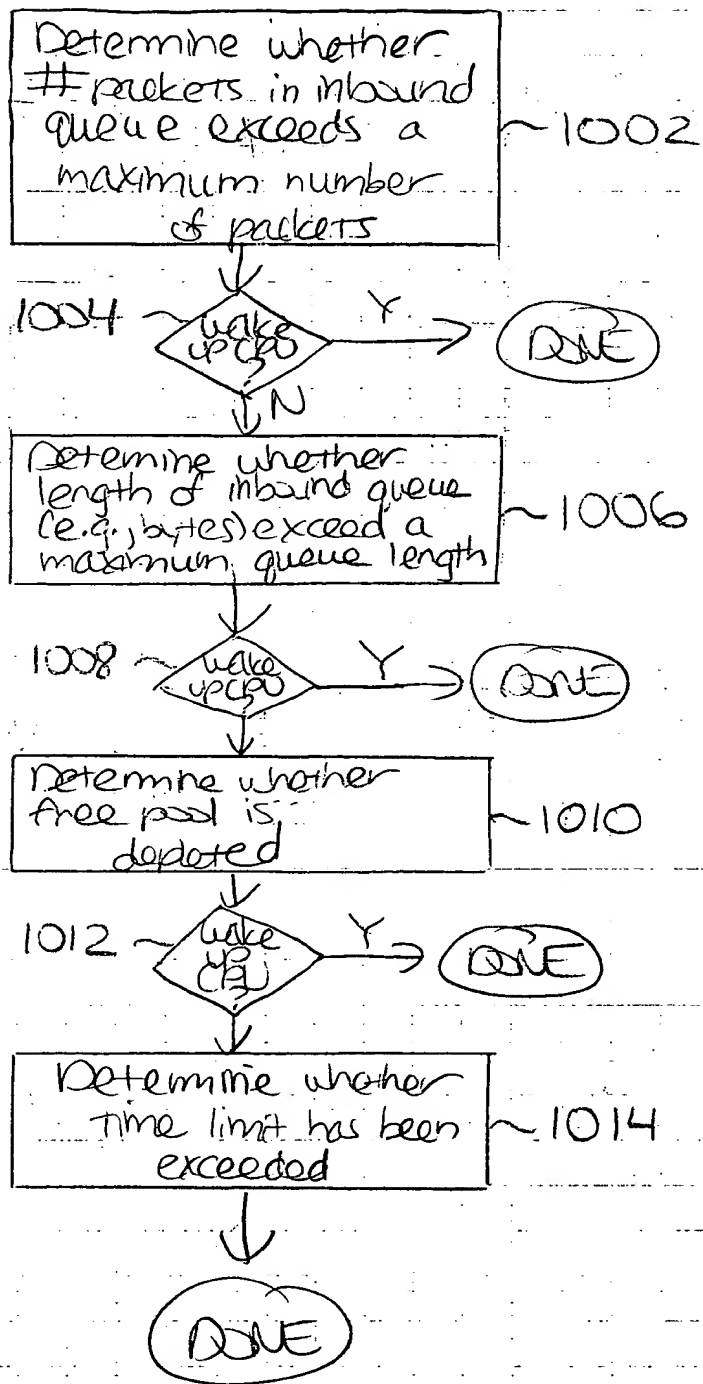
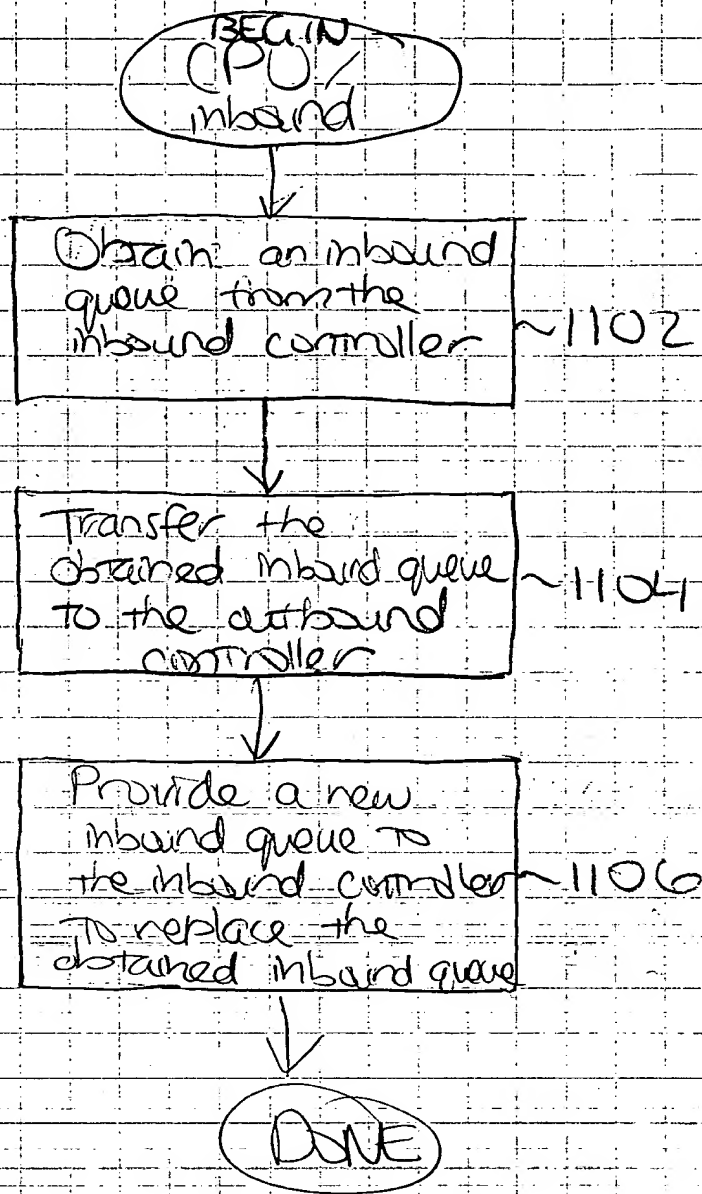


FIG. 10



Obtain an inbound queue from the inbound controller ~ 1102

Transfer the obtained inbound queue to the outbound controller ~ 1104

Provide a new
inband queue to
the inband controller
to replace the
obtained inband queue

ONE

FIG. 11

BEGIN
OUTBOUND
CONTROLLER

Receive notification
from CPU to
handle an inbound
queue

1202

Move the inbound
queue to an
outbound queue

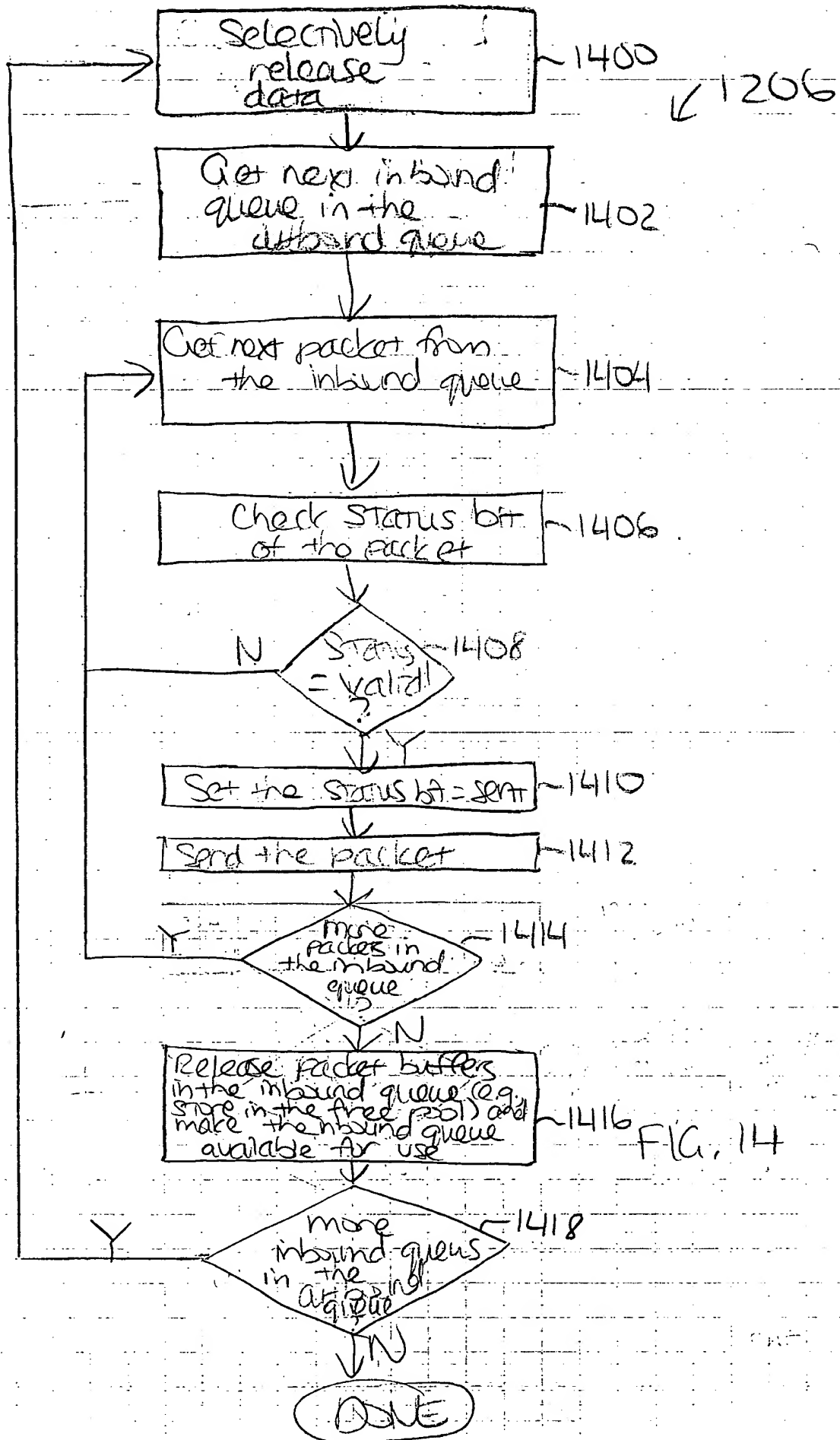
FIG. 13
1204

Transmit packets in
each inbound queue
stored in the
outbound queue

FIG. 14
1206

DONE

FIG. 12



BEGIN
CPU/
outbound

Obtained deallocated
memory (e.g. arr to
an inbound queue)
from the outbound
controller

~1502

Transfer the
obtained deallocated
memory to the
inbound controller

~1504

DONE

FIG. 15

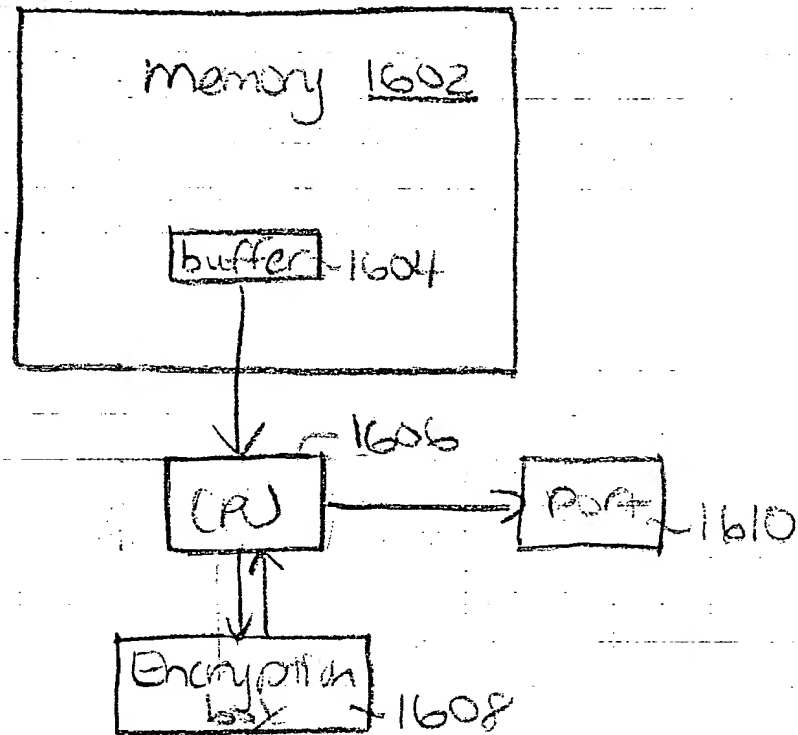
[illegible]

FIG. 16

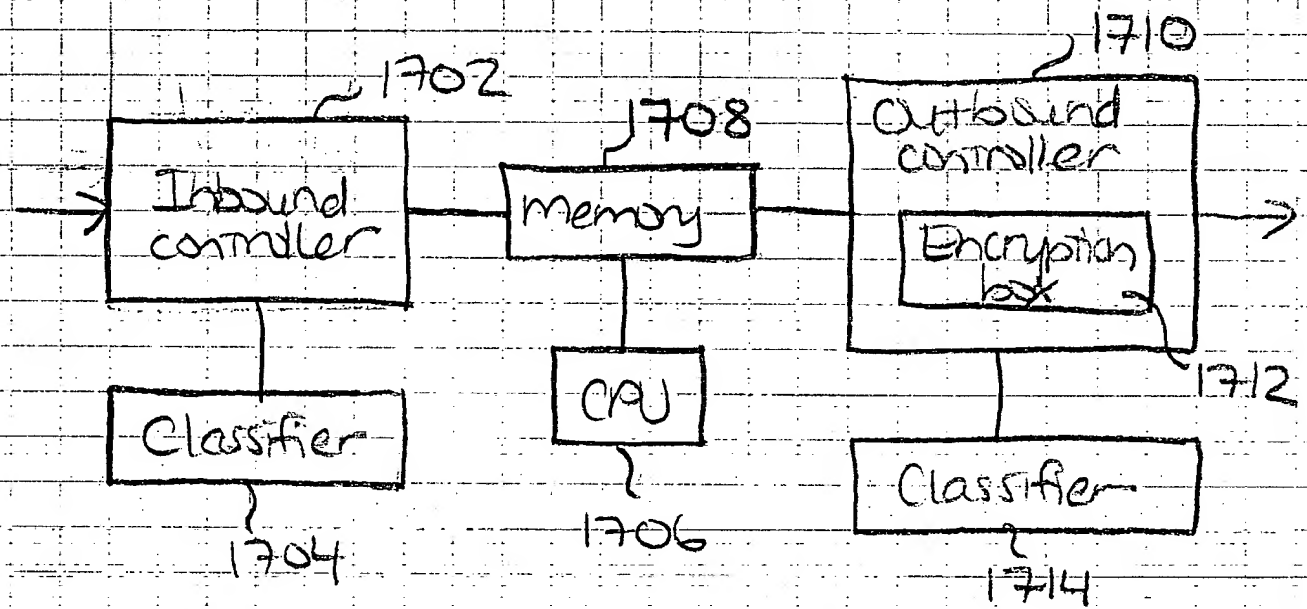


FIG. 17